



Unlimited Ingenuity

For more information and to apply for this position, email us on hr@intelligentmemory.com

Intelligent Memory Limited

www.intelligentmemory.com

Hardware Design Engineer

Job Responsibilities

- Responsible for hardware design of Memory Device Tester and Memory product development projects.
- Work closely with FPGA and ASIC design engineer, to support their design and development of custom FPGA / ASIC to ensure the memory PHYs meet project specifications.
- Work with PCB design Engineer to support the pre- and post- layout simulation.
- Participate in all phases of project including design review sessions.
- Work with the engineering team for the test plan, conduct and supervise the engineering sample testing and validation.
- Work with test Engineers for testing scheme / patterns development for testing in memory product mass production.
- Troubleshoot and debug Hardware issues, develop and maintain technical documentation for the developed hardware, deliver verification plans and reports where needed.

Requirements

- Bachelor's degree or Graduate degree in Electronic Engineering or relevant disciplines.
- Experience and knowledge in high-speed digital electronics design including DRAM circuits, Flash controller and high speed I/O like PCIe, SATA etc.
- Experience in SI and PI simulation using Mentor Graphics or Cadence software tools is a plus.
- Self-motivated and self-directed, able to manage project progress to meet deadline;
- Good problem-solving and interpersonal skills.
- Good team player with ability to communicate technically in a multi-national and multi-cultural working environment.
- Good English language skills, both written and spoken, are needed.